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ested by (name + signature)	Carter Hu Courter Hu	
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esting location	As above	
pplicant's name	SeriousPlayer.com DBA Voltaic Systems	
ddress	Woltaic Systems, New Lab, 19 Morris Avenue, Broo NY 11205	klyn,
lanufacturer's name	····· XXXX REDACTED XXXX	
ddress	XXXX REDACTED XXXX	
est specification :		
itandard	····· UL 2056: 2015	
est procedure	Type approved	
est result	····· Pass	
lon-standard test method	····· N/A	
his test report is specially limited to the	above client company and product model only, it not of Shenzhen TCT Testing Technology Co	nay no
est item description	Power Bank	
rade Mark	VOLTAIC	
lodel/type reference	\/88	
	DC Input: 18V/3A, 25V/3A(max)	
atings	 USB C Input: 5V/1A, 20V/2.25A(max) USB QC Output: 5V/3A, 12V/3A(max) USB C Output: 5V/3A, 20V/2.2A(max) DC Output: 12V/6A, 16V/5A, 19V/4.5A, 24V/3.5A Battony Capacity: 24000mAb, 28 9W/b; 	



Test item particulars:

Information about the product needed to establish a correct test program, such as product mobility, type of power connections and similar.	(Test item particulars are selected by the TRF Originator base on the requirements in the standard)
Designation:	Power Bank
Nominal voltage:	5.0Vdc, 18.0Vdc, 20.0Vdc, 25.0Vdc
Rated capacity:	24000mAh
Output capacity:	1800mAh (for Minimum capacity by DC 24V/3.5A)
Maximum charge voltage:	5.0Vdc, 18.0Vdc, 20.0Vdc, 25.0Vdc
Maximum charge current:	3.0A
Final voltage:	4.5Vdc
Max Ambient Temperature:	45°C max(charge), 45°C max(discharge)
Manufacturer's charge method:	Charging the power bank with 3A until the current reduces to 0.15A.
Possible test case verdicts:	
Test case does not apply to the test object:	N(/A)
Test object does meet the requirement:	P(ass)
Test object does not meet the requirement::	F(ail)
Testing:	
Date of receipt of test item:	2019-10-21
Date(s) of performance of tests:	2019-10-21 to 2019-11-15
General remarks:	
This report shall not be reproduced, except in full, wit	hout the written approval of the testing laboratory.
The test results presented in this report relate only to	the object tested.
"(see remark #)" refers to a remark appended to the r	eport.
"(see appended table)" refers to a table appended to the Throughout this report a point is used as the decimal s	he report.



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(VOLTAIC	LPS	
	Power Bank	Model: V88	
Ś	DC Input: 18V, USB C Input: 5V/ USB QC Output: USB C Output: 5V	/3A, 25V/3A(max) '1A, 20V/2.25A(max) 5V/3A, 12V/3A(max) V/3A, 20V/2.2A(max)	
	DC Output: 12V/6A, 16 Battery Capacity:	24000m4b 88 8Wb:	
	Power Bank Rated c	apacity (min): 1800mAh	
	Date Code: 2019101	0 Made in China	
	Label on th	ne power bank	
CAUTION	 Do not put the battery into Do not attempt to disasse Do not use any chargers Risk of Fire and Burns. Do 45°C/113°F or Incinerate. discontinue use. Follow M 	mble or modify the battery in other than those recommende o Not Open, Crush, Heat Abov Do not short circuit. If bulges anufacturer's Instructions.	any way. ed. ve severely,
	Label on the sm	nallest unit package	
ummany of testing:	(\mathcal{G})		
	Test(s)		
8	General		
84	TABLE: Abnormal Charc	ning Test	
85	TABLE: Abusive Overch	arge Test for model (batter)	a
8.7/8.8	TABLE: Battery Pack Co Surface Temperature Tem	mponent Temperature Test st for model (battery)	and Battery Pack
8.9	TABLE: Limited power s	sources	(\mathcal{S})
8.10	TABLE: Evaluation of vo	oltage limiting components	in SELV circuits
9	Power Input Test		
10	Overload of Output Port	s Test	- All
			(G ·)



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General information:

The product covered in this report is a power bank which consists of eight Li-ion cells (1S8P), the cell (6251100) inside the power bank is approved by TCT according to UL1642 (Report No.: TCT191023B012), see component list table for details and protection circuit provided in the power bank.

Power bank electrical parameter:

	Model	Nominal capacity	Nominal voltage	Nominal Charge Current	Nominal Discharge Current	Maximum Charge Current	Maximum Discharge Current	Maximum Charge Voltage	Cut-off Voltage
Ì)	Ś	5V	USB C Input: 1A	USB QC Output: 3A, USB C Output: 3A	USB C Input: 1A	USB QC Output: 3A, USB C Output: 3A	5V	4.84V
			12V	N/A	USB QC Output: 3A, DC Output: 6A	N/A	USB QC Output: 3A, DC Output: 6A	12V	USB QC Output: 9.78V, DC Output: 9.93V
9	V88	1800mAh (for Minimum	16V	N/A	DC Output: 5A	N/A	DC Output: 5A	16V	9.93V
		DC 24V/3.5A)	18V	DC Input: 3A	N/A	DC Input: 3A	N/A	14V	N/A
			19V	N/A	DC Output: 4.5A	N/A	DC Output: 4.5A	19V	9.93V
Ċ)		(C	20V	USB C Input: 2.25A	USB C Output: 2.2A	USB C Input: 2.25A	USB C Output: 2.2A	20V	9.78V
			24V	N/A	DC Output: 3.5A	N/A	DC Output: 3.5A	24V	9.93V
			25V	DC Input: 3A	N/A	DC Input: 3A	N/A	25V	N/A

Built-in cell el	ectrical paran	neter:		(\mathcal{G})		(\mathcal{O})		(
Model	Nominal capacity	Nominal voltage	Nominal Charge Current	Nominal Discharge Current	Maximum Charge Current	Maximum Discharge Current	Maximum Charge Voltage	Cut-off Voltage
6251100	4000mAh	3.7V	800mA	800mA	2000mA	2000mA	4.25V	3.0V



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		CONSTRUCTION				Р
7		General				Р
7.1	Œ	Power banks shall comply w the Standard for Household Batteries, UL 2054.	ith the requirements in and Commercial	Complied.		Р
7.2		The input port from external general dc jack or USB port, types described in 1.3.	power supply is in and shall not be of the	Complied.		Р
7.3		If the built-in dc/dc converter voltage exceeding 42.4 Vac shall comply with the applica either the Standard for Inforr Equipment – Safety – Part 1 Requirements, UL 60950-1 of Audio/Video, Information and Technology Equipment – Pa Requirements, UL 62368-1.	circuitry generates or 60 Vdc, this circuitry able requirements of nation Technology : General or the Standard for d Communication art 1: Safety	No such voltage exceed 42.4Vac or 60Vdc in the bank.	ing power	N/A
7.4		For power banks with direct following shall be met.	plug-in construction, the	Not direct plug-in constru	uction.	N/A
)	(C	a) The power bank and its be supply shall comply with the of either the Standard for Infe Equipment-Safety-Part 1: Ge UL60950-1 or the Standard to Information and Communicate Equipment-Part 1: Safety Re 1.	uilt-in ac/dc power applicable requirements ormation Technology eneral Requirements, for Audio/Video, tion Technology equirements, UL 62368-		(S)	N/A
)	(C	b) A barrier shall be provided ac/dc power supply and built barrier shall comply with the electrical insulation and fire of Standard for Information Teo Safety-Part 1: General Requ the Standard for Audio/Video Communication Technology Safety Requirements, UL 62	d between the built-in t-in battery pack. The requirements of enclosure of either the chnology Equipment- irements, UL60950-1 or o, Information and Equipment-Part 1: 368-1.			N/A

	PERFORMANCE		Р	
8	General		Р	
8.1	Unless otherwise superseded by a requirement in this Outline, power banks shall comply with the requirements of battery packs in the Standard for Household and Commercial Batteries, UL 2054.	Complied.	Р	T.C.
8.2	For the Abnormal Charging Test and Abusive Overcharge Test in the Standard for Household and Commercial Batteries, UL 2054, 8.3 – 8.5 shall be followed.	S)	P	



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8.3	The tests shall be conducted at the input point of battery protecting circuit. Note – This means dc/dc converter circuitry will be bypassed to result in battery overcharging, which is required for the		Р

		evaluation of protecting circuit.			
8.4	, C	For the Abnormal Charging Test in the Standard for Household and Commercial Batteries, UL 2054, the following shall be taken as maximum current Ic: Rated maximum charging current of the built-in battery (rather than the power bank).	See appended table 8.4	Р	
8.5		For the Abusive Overcharge Test in the Standard for Household and Commercial Batteries, UL 2054, the C5 amp rate of the built-in battery (rather than the power bank) shall be taken for the purpose of this test.	See appended table 8.5	Р	(C)
8.6		For the Battery Pack Component Temperature Test and Battery Pack Surface Temperature Test in the Standard for Household and Commercial Batteries, UL 2054, 8.7 and 8.8 shall be followed.		Р	
8.7		For output loading temperature test, a fully charged power bank shall be discharged. Any load of the output ports that can be operated at the same time shall be considered to result in maximum temperature rise.		Р	
8.8	(C	For input loading temperature test, a fully discharged power bank shall be charged in accordance with manufacturer's specifications. Any load of the output ports that can be operated at the same time shall be considered to result in maximum temperature rise.		P	
8.9		Each output port shall be a limited power source in accordance with the Standard for Household and Commercial Batteries, UL 2054, the Standard for Information Technology Equipment – Safety – Part 1:General Requirements, UL 60950-1, or the Standard for Audio/Video, Information and Communication Technology Equipment – Part 1: Safety Requirements, UL 62368-1, or a Class 2 power source in accordance with the Standard for Class 2 Power Units, UL 1310.	See appended table 8.9	P	C.
8.10	(CC	Each output port shall be a SELV circuit in accordance with the Standard for Information Technology Equipment – Safety – Part 1: General Requirements, UL 60950-1 or be an ES1 in accordance with the Standard for Audio/Video, Information and Communication Technology Equipment – Part 1: Safety Requirements, UL 62368-1.	SELV circuit, dc output rated less than 60Vdc.	P	



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9	Power Input Test	Р
9.1	The current input to a power bank shall not exceed 110% of the marked input current rating of the power bank, when the power bank is operated under the conditions of maximum normal load.	Р
9.2	Maximum normal load shall consist of the maximum current draw while the power bank is operating in all possible modes. This may include charging the built- in battery, and output ports unloaded or loaded at the rated maximum normal load. Any load that can be operated at the same time shall be considered in order to obtain the maximum normal load.	Р

10	Overload of Output Ports Test			Р	
10.1	Each power output pin of output port shall be overloaded in accordance with 10.2 – 10.5.	Tested as required	No.	Р	1
10.2	In accordance with manufacturer's specifications, fully charge the built-in battery of power bank.			Р	
10.3	The power bank is covered with one layer of cheesecloth and placed on a softwood board covered with one layer of tissue paper.	Ś		Р	Ú,
10.4	Each power output pin of output port shall then be loaded to draw the maximum current, for at least 1 h.			P	
10.5	After this test, the cheesecloth and tissue paper shall remain intact.			Ρ	1

11	Flammability of Photovoltaic Cells Test		N/A
11.1	This test shall be conducted if the power bank is provided with integral photovoltaic cells as a power source.	No photovoltaic cells used.	N/A
11.2	In accordance with manufacturer's specifications, fully charge the built-in battery of the power bank.		N/A
11.3	The power bank is covered with one layer of cheesecloth and placed on a softwood board covered with one layer of tissue paper.	(C)	N/A
11.4	The power bank is subjected to single component fault that is likely to occur and which would result in flammability issue of the photovoltaic cells, such as back-feed of battery power, and is kept in this state for 1 h.		N/A
11.5	After this test, the cheesecloth and tissue paper shall remain intact.		N/A

12	Capacity Verification Test	(\mathcal{A})	(\mathcal{C})	Р



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Clause	Requirement + Test	Result - Remark	Verdict

12.1	The marked electrical capacity of power bank, measured at the power output pin of output port, shall comply with the Standard for Secondary Cells and Batteries Containing Alkaline or Other Non-Acid Electrolytes – Secondary Lithium Cells and Batteries for Portable Applications, IEC 61960, Clause 7.3.1, Discharge Performance at 20 °C (Rated Capacity), and the modified test method in 12.2.	See table 12	P	
12.2	The power bank is discharged at a constant current equals to rated current of the output port, until its voltage is equal to the end-of-discharge voltage of the output port, specified by the manufacturer.		P	CC CC

	MARKINGS					
13	General		Р			
13.1	Unless otherwise superseded by a requirement in this Outline, power banks shall comply with the requirements in the Standard for Household and Commercial Batteries, UL 2054.	See marking plate on page 3	Р			
13.2	For electrical ratings, the following information shall be provided	See marking plate on page 3	Ρ			
G	a) Input rating in Vdc and A. If there are more than one input ports, the rating of each port shall be provided;	Two input ports, Input rating in Vdc and A.	Р			
<i>C</i>	b) Output rating in Vdc and A. If there are more than one output ports, it shall include rating of each port and the combined rating (if it is not equal to the summation of all ports); and	Output rating of each port and combined rating marked.	N/A			
	c) Electrical capacity in Ah or mAh. If there are more than one output ports/output ratings, either the capacity of each port/rating shall be provided, or the minimum capacity of these ports/ratings shall be provided.	Capacity of Power bank output port marked (Minimum capacity).	Ρ			

	INSTRUCTIONS		Р
14	General		Р
14.1	Power banks shall be provided with legible instructions pertaining to the proper selection and replacement of its power supply or charger.	User manual provided.	Ρ
14.2	Power banks shall be provided with legible instructions pertaining to a risk of fire or injury to persons associated with the use of the product.	User manual provided.	Р
14.3	An illustration is allowed with a required instruction to clarify the intent but shall not replace the written instruction.		N/A
15	Instructions Pertaining to Risk of Fire or Injury to Persons		Р



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Clause Requirement + Test		Result - Remark	Verdict	
15.1	Instructions pertaining to a risk of fire or injury to persons shall warn the user of reasonably foreseeable risks and state the precautions to be taken to reduce such risks. Such instructions shall be preceded by the heading "INSTRUCTIONS PERTAINING TO RISK OF FIRE OR INJURY TO PERSONS" or the equivalent.	User manual provided.	P	
15.2	Unless otherwise indicated, the text of the instructions in 15.4 shall be in the words specified or words that are equivalent, clear, and understandable. Substitution of the signal word "DANGER" for "WARNING" is allowed when the risk associated with the product is such that a situation exists which, if not avoided, will result in death or serious injury.	User manual provided.	Р	
15.3	Numbering of the items in the list in 15.4 and including other instructions pertaining to a risk of fire or injury to persons that the manufacturer determines to be necessary and that do not conflict with the intent of the instructions are acceptable.	User manual provided.	P P	



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	OL 2030	1	
Clause	Requirement + Test	Result - Remark	Verdict
15.4	The instructions pertaining to a risk of fire or injury to persons shall include those items in the following list that are applicable to the product. The statement "IMPORTANT SAFETY INSTRUCTIONS" or the equivalent shall precede the list, and the statement "SAVE THESE INSTRUCTIONS" or the equivalent shall either precede or follow the list. The word "WARNING" shall be entirely in upper case letters or shall be emphasized to distinguish it from the rest of the text.	User manual provided.	Р
	IMPORTANT SAFETY INSTRUCTIONS		
	WARNING – When using this product, basic precautions should always be followed, including the following:		(
	a) Read all the instructions before using the product.		
	b) To reduce the risk of injury, close supervision is necessary when the product is used near children.		
	c) Do not put fingers or hands into the product.		
	d) Do not expose power bank to rain or snow.		
	e) Use of a power supply or charger not recommended or sold by power pack manufacturer may result in a risk of fire or injury to persons.		(
	f) Do not use the power bank in excess of its output rating. Overload outputs above rating may result in a risk of fire or injury to persons.		
	g) Do not use the power bank that is damaged or modified. Damaged or modified batteries may exhibit unpredictable behavior resulting in fire, explosion or risk of injury.		3)
	h) Do not disassemble the power bank. Take it to a qualified service person when service or repair is required. Incorrect reassembly may result in a risk of fire or injury to persons.		(
	 i) Do not expose a power pack to fire or excessive temperature. Exposure to fire or temperature above 100°C may cause explosion. The temperature of 100°C can be replaced by the temperature of 212°F. 	J (3)
	 j) Have servicing performed by a qualified repair person using only identical replacement parts. This will ensure that the safety of the product is maintained. 		
	k) Switch off the power bank when not in use.		
	SAVE THESE INSTRUCTIONS		



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)	APPENDIX A		Р	1KC
	Standards for Component	ts	Р	
	Standards under which co covered by this outline of include the following:	omponents of the products investigation are evaluated		
	Title of Standard - UL Sta	andard Designation		
~	Automatic Electrical Contr Similar Use, Part 1: Gene 60730-1	rols for Household and ral Requirements – UL		
2	Low-Voltage Fuses – Part – UL 248-1	t 1: General Requirements		C.
	Low-Voltage Fuses – Part – UL 248-14	t 14: Supplemental Fuses		
	Marking and Labeling Sys	stems – UL 969		
	Polymeric Materials – Use Evaluations – UL 746C	e in Electrical Equipment		
	Printed-Wiring Boards – L	JL 796		
Ś	Tests for Flammability of F in Devices and Appliances	Plastic Materials for Parts s – UL 94		.c
	Thermal-Links – Requiren Guide – UL 60691	nents and Application		
	Thermistor-Type Devices	– UL 1434		



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IA						
Object/part no.	Manufacturer/ trademark	Type/model Technical data		Standard	Mark(s) of conformity	
Cell	Dongguan Yongbang Amperex Technology Co., Ltd	6251100	3.7V, 4000mAh	UL 1642	TCT191023B0 12	
Plastic Enclosure	FORMOSA CHEMICALS & FIBRE CORP PLASTICS DIV	AG15A1-H	HB min. thickness: 1.0mm, 60°C	UL 94 UL 746	UL E162823	
Components use	ed on PCB-1					
PCB	AISHENG EXACTITUDE CIRCUITRI CO LTD	RPCB	V-0, 130°C	UL 94 UL 796	UL E248037	
PCB (Alternative)	Interchangeable	Interchangeable	V-0, 130°C	UL 94 UL 796	UL approved	
USB Type-C Controller (U7)	CYPRESS PERFORM	CCG2	Operating voltage: 2.7V to 5.5V, T _A : -40°C ~85°C		Tested with appliance	
Buck-Boost Charger Controller (U8)	SOUTHCHIP SEMICONDUCT OR	SC8802	Input voltage: 2.7V to 30V, Output voltage: 2.0V to 30V		Tested with appliance	
Protective IC (U3)	INJONIC	IP6505	V _{IN} : 4V to 32V V _{OUT} : 3V to 12V		Tested with appliance	
MOSFET (Q6, Q8, Q26)	Alpha and Omega Semiconductor	AO4413	V _{DS} : -30V, V _{GS} : ± 25V, I _D : -70A		Tested with appliance	
Charge management IC (U2)	Unisonic Technologies Company Limited	UCT3687	VDD: 1.8V to 5.5V T _A : -40°C ~ 85°C		Tested with appliance	
MCU (U1)	ABOV Semiconductor Co., Ltd.	MC96F6432SQ	VDD: 7.5V to 28V T _A : -40°C ~85°C		Tested with appliance	
LOW POWER DUAL OPERATIONAL AMPLIFIERS (U4, U9, U11)	STMicroelectroni cs GROUP OF COMPANIES	LM358	V _{CC} : ±16V or ±32V T _{stg} : 0°C ~ 70°C		Tested with appliance	
BOOST CONVERT CONTROL IC (U5)	Feeling Technology Corp.	FP5139	Supply Voltage: 1.8V to 15V T _{OPR} : -10°C ~ 85°C		Tested with appliance	
MOSFET (Q27, Q28, Q29, Q30)	Techcode Semiconductor Limited	TDM3458	V _{DS} : -30V, V _{GS} : ±20V, I _D : -60A		Tested with appliance	



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Components use	components used on PCB-2(for battery charging/discharging)								
PCB	AISHENG EXACTITUDE CIRCUITRI CO LTD	RPCB	V-0, 130°C	UL 94 UL 796	UL E248037				
PCB (Alternative)	Interchangeable	Interchangeable	V-0, 130°C	UL 94 UL 796	UL approved				
Protective IC (U1)	CELLWise	CW1233ALJP	V _{CU} : 4.175V to 4.350V V _{DL} : 2.300V to 3.000V		Tested with appliance				
Protective IC (UB2)	CELLWise	CW1051ALJM	Overcharge Protection: Threshold from 4.100V to 4.4500V, 5mV steps, ±25mV accuracy		Tested with appliance				
Mosfet (QD1, QD2, QC1, QC2)	SHEN ZHEN XIN FEI HONG ELECTRONICS CO., LTD	FH8802	V _{DSS} : 30V, V _{GSS} : ±20V, I _D : 70A		Tested with appliance				
Thermal Protector	YANGZHOU BAOHU ELECTRONIC TECHNOLOGY CO LTD	BH02-BB8D	75°C	UL 60730	UL E489615				
Lead wire	JUNHAO WIRE TECHNOLOGY CO LTD (DONGGUAN)	3239	18AWG, 300V, 200°C	UL 758	UL E357447				
Lead wire	Interchangeable	Interchangeable	18AWG, 300V, 200°C	UL 758	UL approved				

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8.4	TABLE: Abnorma	al Charging Te	est			P
Ambient ter	mperature: 23.3°C		(\mathbf{C})		(\mathcal{S})	
	ld	3	.0		A	
	Ue	4	.5		V	
6	lc		36		А	
N.	Uc		25		V	No.
Sa	mple No.	001	002	003	004	005
Cell Ca	se temp. (°C)	47.8	48.9	49.7	49.4	50.3
Power bar	nk surface temp. (°C)	39.4	40.4	41.2	40.8	42.0
Faulted P	rotective Device		QC1 (pi	n1 to pin8) sh	ort circuit	
Supplemen	itary information:					

1) The DC/DC converter circuit is bypassed with VBUS to VBAT+ was short circuit.

- 2) Charging current is 3 x Ic=36A.
- 3) Charge until the power bank fully charged plus additional 7hrs.

-No explosion or fire, or chemical leak.

8.5	TABLE: Abus	ive Overcharge 1	Fest			Р
Ambient te	emperature: 23.5	5°C				
Sam	ple No.	006	007	008	009	010
ŀ	c(A)	48	48	48	48	24
Cell Cas	e temp. (°C)	58.6	58.7	59.2	57.8	53.4
Power b tem	ank surface p. (°C)	49.7	49.3	40.2	48.9	45.6
Faulted Protective Device		·	QC1 (p	in1 to pin8) sho	ort circuit	·

Supplementary information:

1) The DC/DC converter circuit is bypassed with VBUS to VBAT+ was short circuit.

2) Test current is 10 times C5 for 4pcs and 5 times C5 for 1pcs.

3) The test was continued until the temperature of the internal cell casing returns to ambient.

-No explosion or fire.

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Power bank Co	mponent Temper	ature Test (for Charg	ing)	(\mathbf{C})	. (
Sample No.		011	012		(
Testing	DC	C Input	USB C In	put	Limited T	
Process	18V	25V	5V			
PCB near U1 (for PCB-2)	42.5	43.2	42.3	42.9	130-45+24=109	
PCB near QD2	40.6	41.6	40.4	40.9	130-45+24=109	
PCB near UB2	33.3	33.9	33.1	33.6	130-45+24=109	
PCB near U1 (for PCB-1)	41.1	42.2	40.8	41.3	130-45+24=109	
PCB near U2	35.9	36.7	35.8	36.2	130-45+24=109	
PCB near U7	38.2	39.1	37.9	38.6	130-45+24=109	
PCB near U8	36.8	37.6	36.7	37.2	130-45+24=109	
Lead Wiring	33.8	34.7	33.6	34.2	200-45+24=179	
Cell body	33.0	33.8	32.7	33.4	Ref.	
PCB near USB port	38.3	39.1	38.0	38.7	130-45+24=109	
Ambient (°C)	24.0*	24.0*	24.0*	24.0*		
Power bank Co	mponent Temper	ature Test (for Charg	ing)			
Sample No.		011	012			
Testing	DC	C Input	USB C Input		Limited T	
Process	18V	25V	5V	20V	(
Power bank surface (near USB port)	31.3	31.6	31.3	31.6	60-45+24=39	
Power bank surface (near cell)	29.1	29.4	29.1	29.4	60-45+24=39	
Ambient (°C)	24.0*	24.0*	24.0*	24.0*		
Supplementary 1) Charging at 2) Charging at Remark:	information: 18V/3A, 25V/3A τ 5V/3A, 20V/2.5A	using DC Input; using USB C Input;				

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	Surface Terr	perature res	st for model	2				
Power bank C	Component Te	mperature Te	est (for Disch	arging)	($\langle \mathbf{O} \rangle$	(
Sample No.	ample No. 011							
Testing	USB QC	Output	USB C	Output	DC C	Dutput	Limited T	
Process	5V/3A	12V/3A	5V/3A	20V/2.2A	12V/6A	24V/3.5A		
PCB near U1 (for PCB-2)	42.7	43.0	43.4	44.6	53.1	61.0	130- 45+24=109	
PCB near QD2	40.9	41.9	40.7	43.2	42.3	50.5	130- 45+24=109	
PCB near UB2	28.7	28.9	28.9	30.4	39.4	48.6	130- 45+24=109	
PCB near U1 (for PCB-1)	43.4	44.8	43.5	45.4	54.7	61.6	130- 45+24=109	
PCB near U2	33.9	34.6	34.2	35.8	44.8	50.7	130- 45+24=109	
PCB near U7	29.8	29.9	29.6	31.3	41.2	47.1	130- 45+24=109	
PCB near U8	30.1	30.8	30.2	32.3	41.2	47.9	130- 45+24=109	
Lead Wiring	28.1	28.4	28.2	29.8	38.8	45.8	200- 45+24=179	
Cell body	27.9	29.3	28.2	30.4	39.7	46.7	Ref.	
PCB near USB port	41.2	42.2	41.3	42.9	50.4	53.3	130- 45+24=109	
Ambient (°C)	24.0*	24.0*	24.0*	24.0*	24.0*	24.0*		
Power bank C	Component Te	mperature Te	est (for Disch	arging)				
Sample No.			0,	11				
Testina	USB QC	Output	USB C	Output	DC C	Dutput	Limited T	
Process	5V/3A	12V/3A	5V/3A	20V/2.2A	12V/6A	24V/3.5A		
Power bank surface (near USB port)	27.4	27.6	27.3	27.9	33.2	33.9	60-45+24=39	
Power bank surface (near cell)	26.9	27.2	26.8	27.7	30.8	31.6	60-45+24=39	
Ambient (°C)	24.0*	24.0*	24.0*	24.0*	24.0*	24.0*		

-Component & surface temperature not exceed the limits.

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	Temperature Test			
Power bank	Component Temperature Test (fo	r Charging & Discharging at the s	same time)	
Sample No	. 011	012	Limited T	
Testing Process	DC Input (25V/2.7A) & USB QC Output (12V/2.5A) & USB C Output (20V/2.5A) & DC Output (24V/3.5A)	USB C Input (20V/2.5A) & USB QC Output (12V/2.5A) & DC Output (24V/3.5A)		
PCB near U (for PCB-2)	1 56.4	64.9	130-45+24=109	
PCB near QD2	45.6	54.2	130-45+24=109	
PCB near UB2	43.9	51.5	130-45+24=109	
PCB near U (for PCB-1)	57.9	65.8	130-45+24=109	
PCB near U	2 46.1	53.4	130-45+24=109	
PCB near U	7 43.5	51.3	130-45+24=109	
PCB near U	8 43.7	50.6	130-45+24=109	
Lead Wiring	41.3	49.7	200-45+24=179	
Cell body	42.9	50.2	Ref.	
PCB near USB port	51.7	55.6	130-45+24=109	
Ambient (°C	24.0*	24.0*		
Power bank	Component Temperature Test (fo	r Charging & Discharging at the	same time)	
Sample No	. 011	012	Limited T	
Testing Process	DC Input (25V/2.7A) & USB QC Output (12V/2.5A) & USB C Output (20V/2.5A) & DC Output (24V/3.5A)	USB C Input (20V/2.5A) & USB QC Output (12V/2.5A) & DC Output (24V/3.5A)		
Power ban surface (nea USB port)	34.7	35.4	60-45+24=39	
Power ban surface (nea cell)	ar 31.3	32.4	60-45+24=39	
Ambient (°C	3) 24.0*	24.0*		

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8.9	TAE	BLE: Limited po	ower sources				Р
Circuit ou	tput te	sted:					(
Note: Me	asured	Uoc (V) with all	load circuits d	isconnected:		(<u>k</u> G')	4
Compon	ents	Sample No.	Uoc (V)	١	_{sc} (A)		VA
				Meas.	Limit	Meas.	Limit
Normal (l QC Outpu 5V)	JSB ut	013	5.20	3.110	8	15.230	<u></u> ≤100
QD1 (pin pin8) SC QD1 (pin pin8) SC	1 to 1 to	013	5.21	3.142	8	15.337	≤100
Normal (l QC Outpu 12V)	JSB ut	014	12.13	3.072	8	15.274	≤100
QD1 (pin pin8) SC (USB QC Output 12	1 to 2V)	014	12.14	3.167	8	15.394	≤100
Normal (l C Output	JSB 5V)	015	5.23	3.011	8	14.270	≤100
QD1 (pin pin8) SC (USB C Output 5\	1 to /)	015	5.23	3.075	8	15.822	≤100
Normal (U C Output 20V)	JSB	016	20.13	3.024	8	14.629	≤100
QD1 (pin pin8) SC (USB C Output 20	1 to DV)	016	20.15	3.036	8	15.696	≤100
Normal (I Output 12	DC 2V)	017	12.20	6.549	8	74.950	≤100
QD1 (pin pin8) SC Output 12	1 to (DC 2V)	017	12.25	6.715	8	76.671	≤100
Normal (I Output 24	DC 4V)	018	24.26	3.463	8	88.509	≤100
QD1 (pin pin8) SC Output 24	1 to (DC 4V)	018	24.24	3.667	8	89.654	≤100
suppleme	entary i	nformation:					•

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9	TABLE: F	Power Input Test			Р
U (V)	I (A)	Irated (A)	P (W)	Condition/status	
18 (for DC Input)	2.88	3.0		Charging with internal cells disc end-of voltage	charged to
25 (for DC Input)	2.89	3.0		Charging with internal cells disc end-of voltage	charged to
5 (for USB C)	1.998	1.0		Charging with internal cells disc end-of voltage	charged to
12 (for USB-C)	2.249	2.25		Charging with internal cells disc end-of voltage	charged to

Supplementary information:

-The input to power bank not exceeds 110% of the marked input current rating. **Remark:** When charging the sample can be supported simultaneous discharging.

Ambient temperature: 23.4	4°C	9		\mathbf{O}		
USB QC Output Port						
Sample No.	1	026	027	028	029	030
Discharge surrent (mA)	5V	3000 🔇	3000	3000	3000	3000
Discharge current (mA)	12V	3000	3000	3000	3000	3000
Conscitu (m Ab)	5V	15047	15374	15098	15105	15241
Capacity (mAn)	12V	6816	6878	6897	6834	6877
Rated capacity Min. ((mAh)			1800	•	
JSB C Output Port						
Sample No.		031	032	033	034	035
	5V	3000	3000	3000	3000	3000
Discharge current (mA)	20V	2200	2200	2200	2200	200
Canacity (mAh)	5V	15275	15036	15064	15138	15167
Capacity (mAn)	20V	4230	4345	4651	4465	4354
Rated capacity Min. (mAh)			1800		
DC Output Port	6					
Sample No.		024	025	026	027	028
	12V	6000	6000	6000	6000	6000
Discharge current (mA)	24V	3500	3500	3500	3500	3500
	12V	3941	4035	4124	3988	4065
Capacity (mAn)	24V	1863	1897	1864	1834	1868
Rated capacity Min. (mAh)			1800		
Supplementary information	i: and complia	d	3	((



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Photo Documentation





*** End of Test Report ***